1. Diferencia entre arquitectura de von neuman y la de harvard

<https://www.microcontrollertips.com/whats-the-difference-between-von-neumann-and-harvard-architectures/>

<http://www.differencebetween.net/technology/difference-between-von-neumann-and-harvard-architecture/>

The Von Neumann bottleneck has increased over time because processors have improved in speed while memory has not progressed as fast. Some techniques to reduce the impact of the bottleneck are to keep memory in cache to minimize data movement, hardware acceleration, and speculative execution. It is interesting to note that speculative execution is the conduit for one of the latest security flaws discovered by Google Project Zero, named Spectre.

1. 5 Componentes principales explicados

<https://byte-notes.com/five-basic-components-computer-system/>

1. Diferencia entre arquitecturas CISC, RISC, VLIW y MISC??

<https://www.edgefxkits.com/blog/what-is-risc-and-cisc-architecture/>

<https://www.youtube.com/watch?v=07cpxBfy7JI>

<https://www.youtube.com/watch?v=mDrUkjOVtAU>

1. Explicacion de memoria

<https://www.explainthatstuff.com/how-computer-memory-works.html> chapter 5

1. Explicación memoria caché LOGICAL STRUCTURE OF THE MEMORY

<https://www.youtube.com/watch?v=yi0FhRqDJfo> chapter 6

1. Explicacion memoria ram-rom

<https://www.youtube.com/watch?v=PVad0c2cljo>

<https://www.youtube.com/watch?v=nuQ26EgJYSA>

1. Parallel computing

<https://en.wikipedia.org/wiki/Parallel_computing>

<https://www.youtube.com/watch?v=q7sgzDH1cR8>

1. Overview of microprocessor

<http://www.science.smith.edu/~jcardell/Courses/EGR328/Readings/uProc%20Ovw.pdf>

1. CPU buying (procesadores actualizados)

<https://www.tomshardware.com/reviews/cpu-buying-guide,5643.html>

<https://www.youtube.com/watch?v=1vi1dIuSsa4>

1. Historia

<https://www.youtube.com/watch?v=-M6lANfzFsM>

<https://www.livescience.com/20718-computer-history.html>